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Application Information

Title Line One:: INTERCONNECT SUBSTRATE,
Title Line Two:: SEMICONDUCTOR DEVICE, METHODS OF
Title Line Three:: FABRICATING, INSPECTING, AND
Title Line Four:: MOUNTING THE SEMICONDUCTOR DEVICE,
Title Line Five:: CIRCUIT BOARD, AND ELECTRONIC
Title Line Six:: INSTRUMENT
Total Drawing Sheets:: 7
Docket Number:: 109609

Continuity Information

>This application is a:: 371 of
Application One:: PCT/JP00/06824
Filing Date:: September 29, 2000

Prior Foreign Applications

Foreign Application One:: 11-281424
Filing Date:: October 1, 1999
Country:: JAPAN
Priority Claimed:: YES

Assignee Information

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